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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*

None

## \*\* FOREIGN APPLICATIONS \*\*\*\*

JAPAN 2003-186827 06/30/2003  
JAPAN 2003-348750 10/07/2003

VERIFIED

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/24/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	JAPAN	6	17	2
Verified and Acknowledged	<i>T. J. Wenderoth</i> <i>TJ</i> Examiner's Signature Initials				

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## TITLE

Hetero-junction bipolar transistor and manufacturing method thereof

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
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